AMENDMENTS TO THE CLAIMS

The listing of claims will replace all prior versions, and listings, of claims in the application:

Listing of Claims:

1. (currently amended) A method comprising:

   sending bits of data on an input line and on a control line of a test access protocol (TAP) controller of a JTAG-compliant device; and

   receiving and storing the bits of data from the input line and from the control line at the TAP controller in response to a burst-write instruction being an active instruction in the TAP controller.

2. (original) The method of Claim 1, wherein the receiving and storing are performed in response to the TAP controller being in a shift data register state or a pause data register state.

3. (canceled)

4. (currently amended) The method of Claim [[3]] 1, further comprising loading the burst-write instruction into an instruction register of the TAP controller.

5. (currently amended) The method of Claim 1, further comprising receiving and storing bits in response to:

   the TAP controller being in an Exit1 data register state~~, and~~

   ~~a burst-write instruction being an active instruction in the TAP controller~~.

6. (currently amended) The method of Claim 1, further comprising receiving and storing bits in response to:

   the TAP controller being in an Exit2 data register state~~, and~~

~~a burst-write instruction being an active instruction in the TAP controller~~.

7. (original) The method of Claim 1, wherein the storing the bits comprises shifting the bits into a least significant bit of a data register.

8. (currently amended) An apparatus comprising:

a JTAG-compliant test access protocol (TAP) controller;

a control (TMS) line connected to the TAP controller; and

an input (TDI) line connected to the TAP controller,

the control and input lines adapted to transmit data bits, and

the TAP controller adapted to receive and store the data bits transmitted on the control and input lines <u>in response to a burst-write instruction being an active instruction in the TAP controller</u>.

9. (original) The apparatus of Claim 8, wherein the TAP controller is adapted to receive the data bits in response to the TAP controller being in a shift data register (Shift-data-register) state or a pause data register (Pause-data-register) state.

10. (canceled)

11. (original) The apparatus of Claim 8, further comprising a data register accessible by the TAP controller, the data register adapted to store the data bits received from the control line and from the input line.

12. (original) The apparatus of Claim 11, wherein the data register is adapted to store the data bits by shifting the data bits into a least significant bit of the data register.

13. (currently amended) The apparatus of Claim 8, wherein the TAP controller is adapted to receive the data bits of in response to:

the TAP controller being in an Exit1 data register state~~, and~~

~~a burst-write instruction being an active instruction in the TAP controller~~.

14. (currently amended) The apparatus of Claim 8, wherein the TAP controller is adapted to receive the data bits of in response to:

the TAP controller being in an Exit2 data register state~~, and~~

~~a burst-write instruction being an active instruction in the TAP controller~~.

15. (currently amended) A computer program product stored on a computer operable media, the computer program product comprising software code effective to:

send bits of data on an input line and on a control line of a test access protocol (TAP) controller of a JTAG-compliant device; and

receive and store the bits of data from the input line and from the control line at the TAP controller <u>in response to a burst-write instruction being an active instruction in the TAP controller</u>.

16. (original) The computer program product of Claim 15, wherein the software code effective to receive and store are each performed in response to the TAP controller being in a shift data register state or a pause data register state.

17. (canceled)

18. (currently amended) The computer program product of Claim [[17]] <u>15</u>, further comprising software code effective to load the burst-write instruction into an instruction register of the TAP controller.

19. (currently amended) The computer program product of Claim 15, further comprising software code effective to receive and store the data bits in response to:

the TAP controller being in an Exit1 data register state~~, and~~

~~a burst-write instruction being an active instruction in the TAP controller~~.

20. (currently amended) The computer program product of Claim 15, further comprising software code effective to receive and store the data bits in response to:

the TAP controller being in an Exit2 data register state~~, and~~

~~a burst-write instruction being an active instruction in the TAP controller~~.

21. (original) The computer program product of Claim 15, wherein the software code effective to store the bits comprises software code effective to shift the bits into a least significant bit of a data register.

REMARKS - Amendment to the Drawings

The Office Action objected to Figure 2 for two reasons. First, the Office Action contends that Figure 2 depicts the prior art. Applicant respectfully disagrees. While the actual drawing is substantially similar to a prior art drawing provided by the IEEE, the detailed description in Applicant's specification describing Figure 2 is directed, in part, to Applicant's claimed invention. Therefore, Figure 2 is not "prior art," as contended in the Office Action. As agreed during the Examiner Interview, discussed above, Applicant's agree to note that Figure 2 depicts an "overview" of a logic state diagram. This notation has been provided in the revised Figure 2 as well as in the specification (brief description of the drawings and in the detailed description). Therefore, Applicants respectfully request that the Examiner withdraw the objection to Figure 2 in this regards in the next Office Communication.

Second, the Office Action objected to Figure 2 as including two mistakes. Applicant notes with great appreciation that the Examiner identified these two typographical errors. The Examiner will observe that these typographical errors have also been corrected in the replacement sheet of Figure 2.